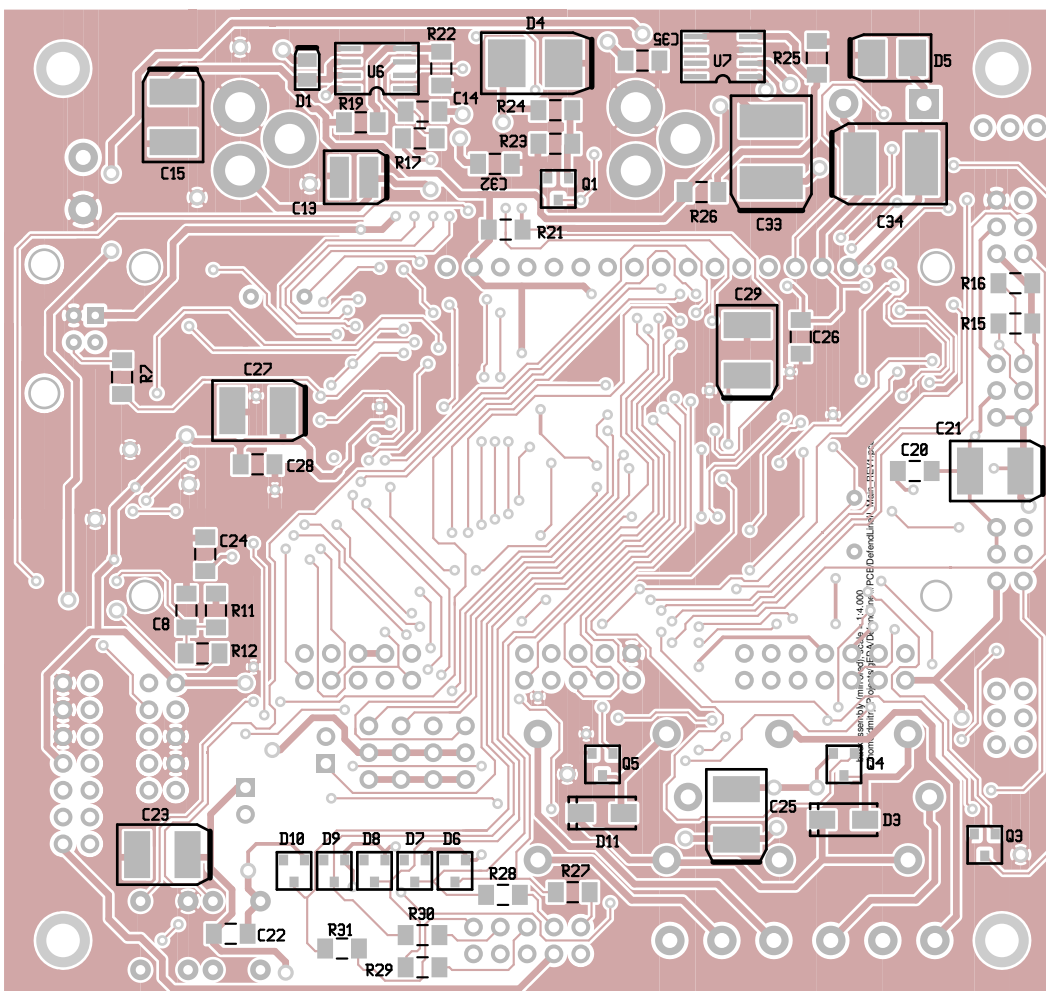


Pict.1 DLII Main Module, component layout, top side



Pict.2 DLII Main Module, component layout, bottom side