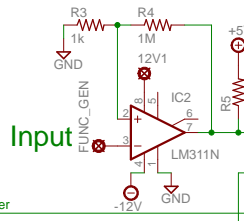
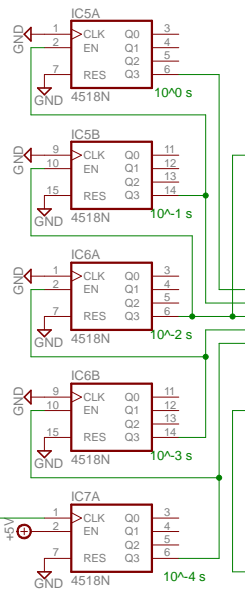


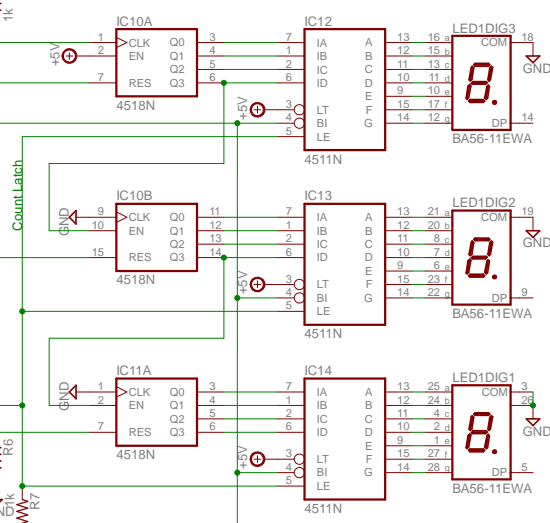
Schmitt Trigger



Reference Periods



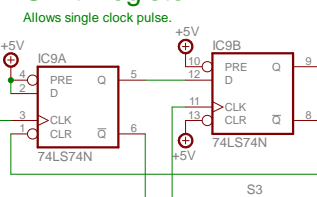
Func Gen Count



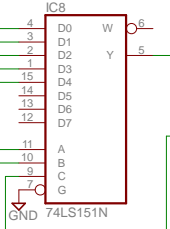
Frequency Display

Shift Register

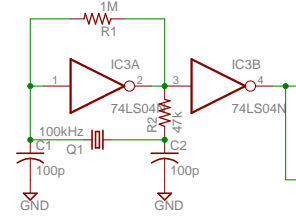
Allows single clock pulses.



Multiplexer



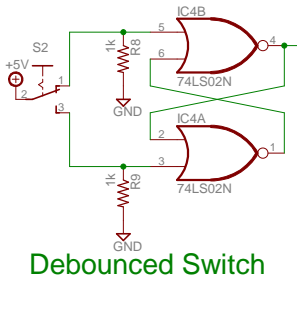
Stable Clock 100kHz



Latch Override

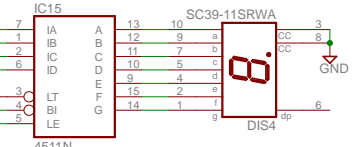


Debounced Switch



Counter Resets at 0101=5

Order of Magnitude Display



TOBIAS BROWN-HEFT

PHYS 432

JUNE 2012