OP-Code	Assembler instruction	Meaning
1	ADD	Add (register content A and B) and store it in register A
10	AND	Bitwise AND-linkage (register content A and B)
11	JA<B	Jump, if A < B
100	JA>B	Jump, if A > B
101	DECA	Decrement A(-1)
110	DECB	Decrement B (-1)
111	DIV	Divide (register content A and B)
1000	HLT	Halt the processor
1001	INCA	Increment A (+1)
1010	INCB	Increment B (+1)
1011	JE	Jump, if A = B
1100	JMP	Jump for no reason
1101	LDA	Store a value in A
1110	LDB	Store a value in B
1111	LOOP	Decrement A and jump, if A != 0
10000	MUL	multiply (register content A and B)
10001	NOT	Bitwise NOT-linkage (register content A)
10010	OR	Bitwise OR-linkage (register content A and B)
10011	OUTA	Output A
10100	OUTB	Output B
10101	SUB	Subtract (register content A and B)
10110	DELAY	Delay of 1 second