REYAX
TECHNOLOGY CORPORATION, LTD

15-AUG-2021 56312E33

RYRR10S

Multiprotocol Fully

Integrated 13.56MHz

RFID & NFC Antenna Module

Datasheet

PRODUCT DESCRIPTION

The RYRR10S antenna module is a 13.56-MHz RFID and Near Field Communication (NFC) system. Built-in programming options make the device suitable for a wide range of applications for proximity and vicinity identification systems.

FEATURES

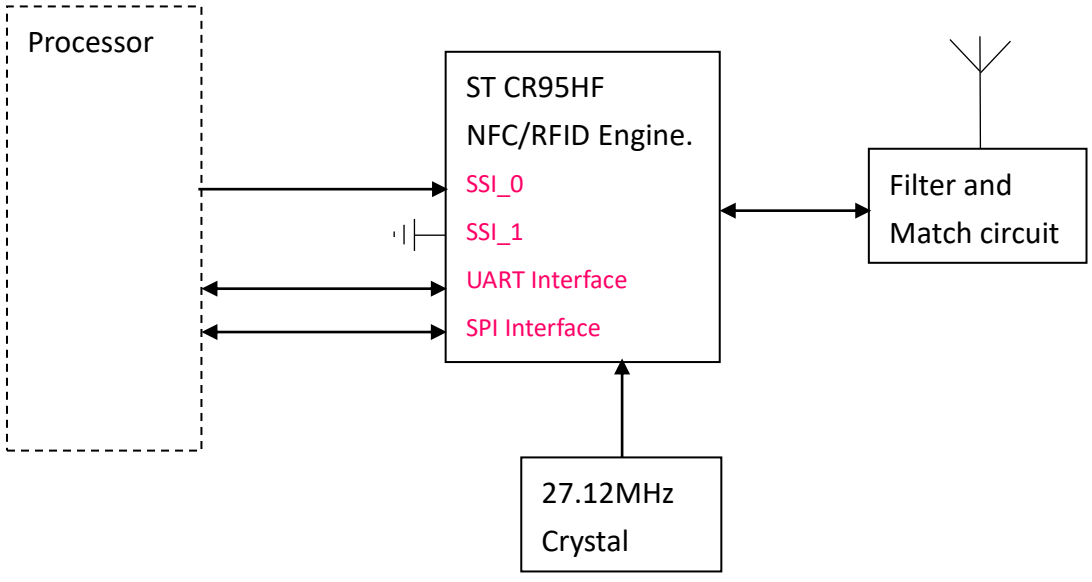
- ST CR95HF NFC/RFID Engine.
- Supports Near Field Communication (NFC) ISO/IEC 18092.
- Completely Integrated Protocol Handling for ISO/IEC 15693, ISO/IEC 14443A, ISO/IEC 14443B.
- NXP MIFARE® Classic compatible
- Designed with PCB integrated antenna.
- UART / SPI Interface optional.

SPECIFICATION

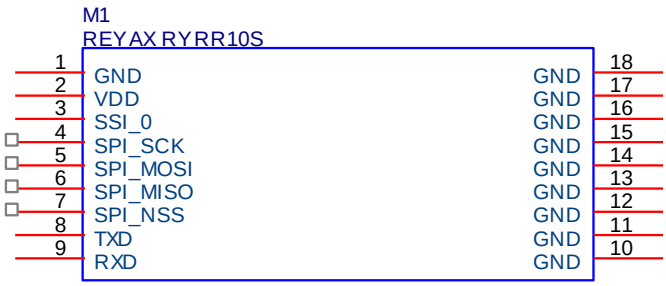
Item	Min.	Typical	Max.	Unit	Condition
Operation Voltage	2.7		5.5	V	VDD
V_{OH}	0.9*VDD	VDD	VDD	V	TXD High-level output voltage
V_{OL}	0	0	0.1*VDD	V	TXD Low-level output voltage
V_{IH}	0.7*VDD	3.3	VDD	V	RXD High-level input voltage
V_{IL}	0	0	0.3*VDD	V	RXD Low-level input voltage
RF Output Power		55		mW	VDD=3V
Reader mode current		70		mA	VDD=3V
Ready state current		2.5		mA	VDD=3V
Tag detect mode current		50	100	uA	4 RF bursts per second
Sleep state current		20	80	uA	
Communication Range		4	8	cm	Depending on the RFID TAG.
Startup time		6		ms	
Baud Rate		57600		bps	8,N,1
RF Frequency Range	13.553	13.56	13.567	MHz	
Operating Temperature	-25	25	+85	˚C	
Antenna					Internal
Weight		4		g	

***For more detail, please refer to the ST CR95HF information.**

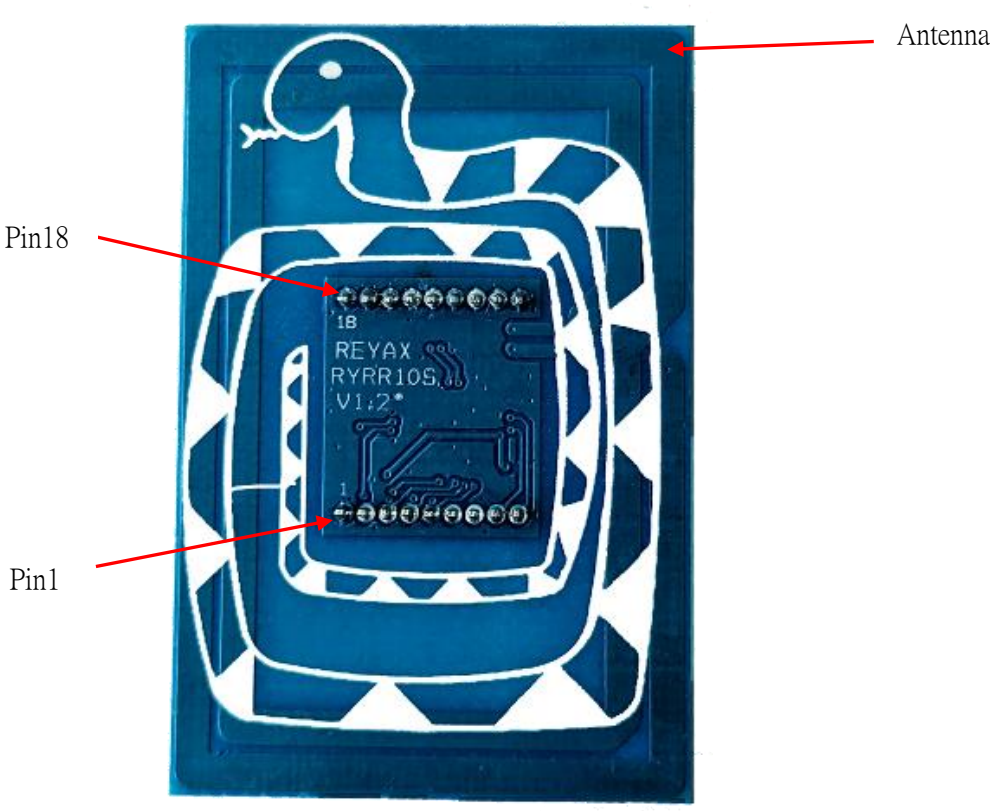
BLOCK DIAGRAM



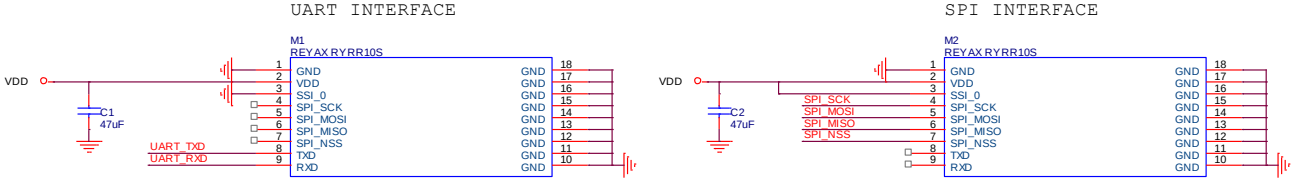
PIN DESCRIPTION



Pin	Name	I/O	Condition
1	GND	P	Ground
2	VDD	P	Main power supply
3	SSI_0	I	Select serial communication interface UART: 0 SPI: 1
4	SPI_SCK	I	SPI serial clock
5	SPI_MOSI	I	SPI Data, Slave Input
6	SPI_MISO	O	SPI Data, Slave Output
7	SPI_NSS	I	SPI Slave Select (active low)
8	TXD	O	UART Data Output
9	RXD	I	UART Data Input
10	GND	P	Ground
11	GND	P	Ground
12	GND	P	Ground
13	GND	P	Ground
14	GND	P	Ground
15	GND	P	Ground
16	GND	P	Ground
17	GND	P	Ground
18	GND	P	Ground



APPLICATION SCHEMATIC



PCB LAYOUT GUIDE

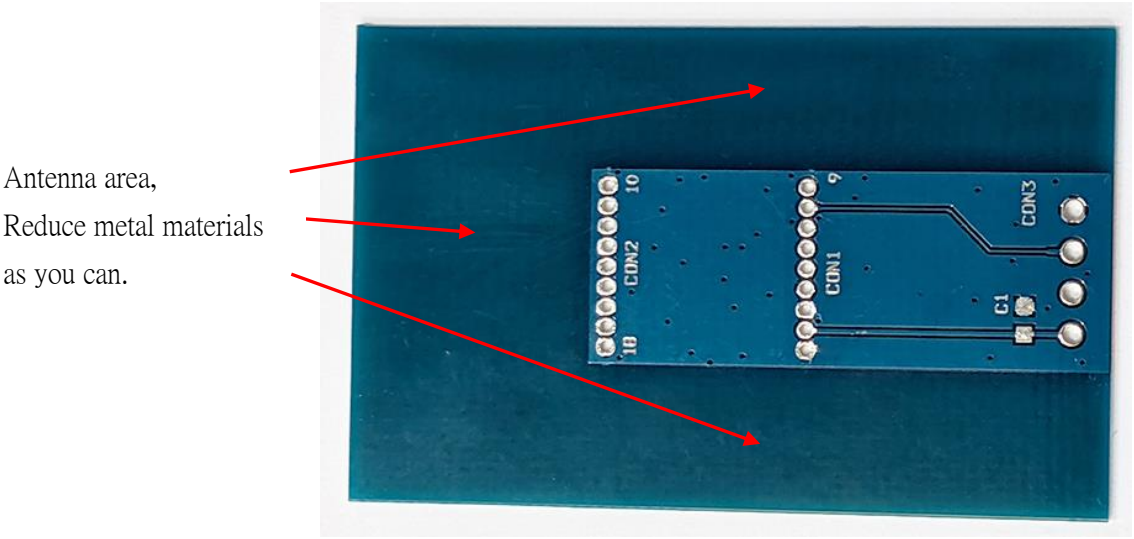
[1] Avoid placing any metal material between the RYRR10S and the RFID TAG.

[2] The height >1mm components and any metal material should be placed at least 5 mm away from the RYRR10S.

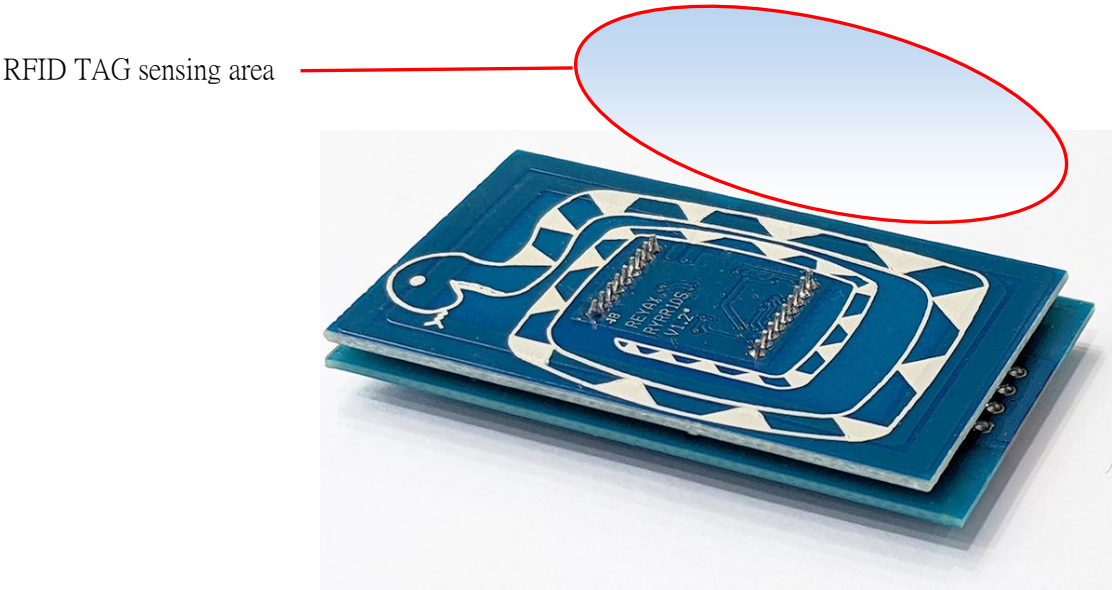
[3] The minimum distance between the plastic cover and the RYRR10S should be 1mm.

[4] Keep the VDD voltage ripple under 30mVpp.

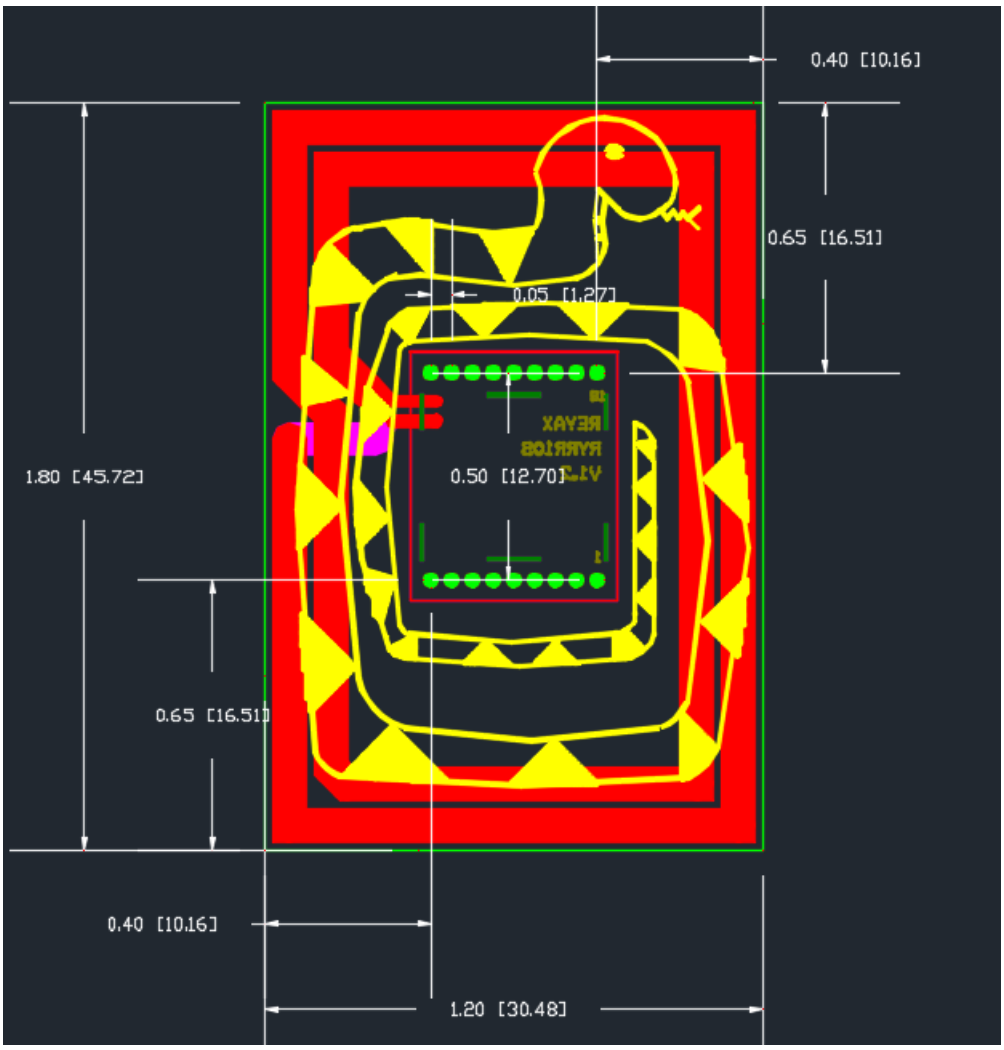
[5] PCB layout reference:



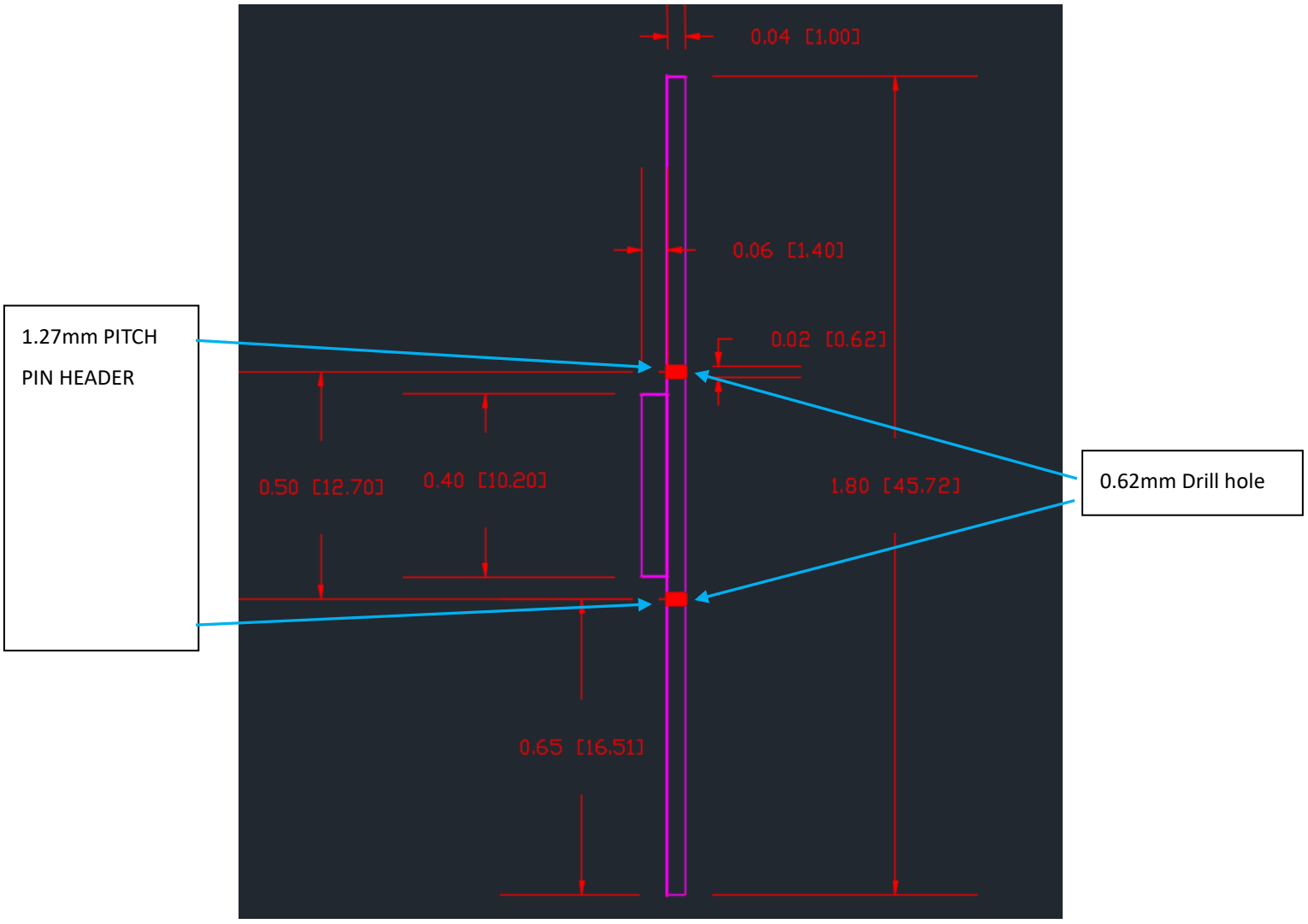
[6] RYRR10S assembled state:



DIMENSIONS

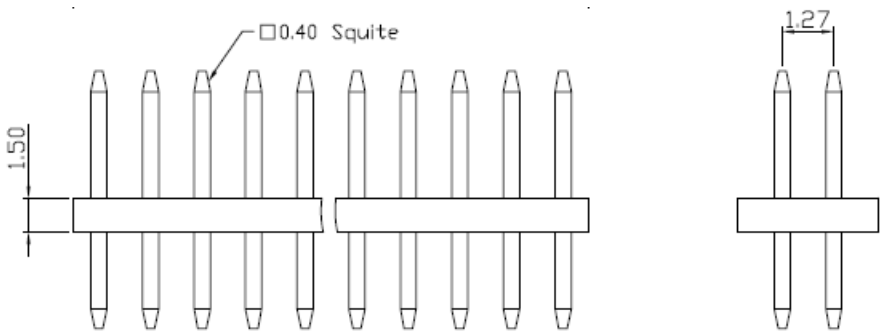


unit : inch[mm]



unit : inch[mm]

1.27mm PITCH PIN HEADER



unit : mm

FIRMWARE QUICK START GUIDE

Example1 : ISO/IEC 14443A READ UID Command in HEX.

55

09 04 68 01 07 10

09 04 68 01 07 00

02 04 02 00 02 80

09 04 3A 00 58 04

09 04 68 01 01 D3

04 02 26 07

04 03 93 20 08

Example2 : ISO/IEC 15693 READ UID Command in HEX.

55

09 04 68 01 07 10

09 04 68 01 07 00

02 02 01 0D

04 03 26 01 00

Sleep state command

00 07 0E 08 01 00 38 00 18 00 00 60 00 00 00 00 00

Hibernate state command

00 07 0E 08 04 00 04 00 18 00 00 00 00 00 00 00 00

***For more detail, please refer to the ST CR95HF information.**

ORDER INFORMATION

Ordering No.	Pin Header
RYRR10S	2 X 1.27mm pin header
RYRR10S_NP	Not mount

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