```
entity sev seg decoder is
    Port ( binary num : in STD LOGIC VECTOR (3 downto 0);
            ABCDEFG : out STD_LOGIC_VECTOR (6 downto 0));
end sev_seg_decoder;
architecture Behavioral of sev seg decoder is
begin
process(binary num)
begin
case binary_num is
when "0000" => ABCDEFG <="0000001";</pre>
when "0001" => ABCDEFG <="1001111";</pre>
when "0010" => ABCDEFG <="0010010";</pre>
when "0011" => ABCDEFG <="0000110";</pre>
when "0100" => ABCDEFG <="1001100";
when "0101" => ABCDEFG <="0100100";</pre>
when "0110" => ABCDEFG <="1100000";</pre>
when "0111" => ABCDEFG <="0001111";</pre>
when "1000" => ABCDEFG <="0000000";</pre>
when "1001" => ABCDEFG <="0001100";</pre>
when others => ABCDEFG <="0111000";</pre>
END CASE;
END PROCESS;
end Behavioral;
```