



Clock pulse generator

Note

Replaced IC 7414 with RC Network to reduce debouncing effect
 7411 has triple 3 input AND gate, it's divided into 3 units A,B,C
 diodes near the switch is used to avoid reset on adjust the time
 Other diodes are used to separate the TTL logic and CMOS logic

Display driver / Working Logic

A digital clock without a microcontroller		
Neutrino		
Sheet: /		
File: counter.sch		
Title: Digital Clock		
Size: A4	Date: 2020-10-01	Rev: V2
KiCad E.D.A. kicad (5.1.6)-1		Id: 1/1