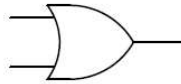


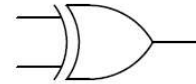
**AND**

A	B	Output
0	0	0
0	1	0
1	0	0
1	1	1



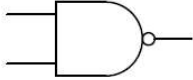
**OR**

A	B	Output
0	0	0
0	1	1
1	0	1
1	1	1



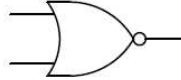
**XOR**

A	B	Output
0	0	0
0	1	1
1	0	1
1	1	0



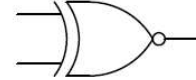
**NAND**

A	B	Output
0	0	1
0	1	1
1	0	1
1	1	0



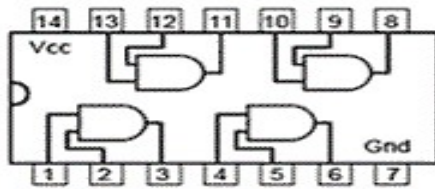
**NOR**

A	B	Output
0	0	1
0	1	0
1	0	0
1	1	0

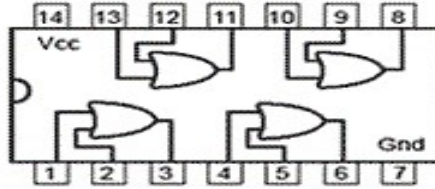


**XNOR**

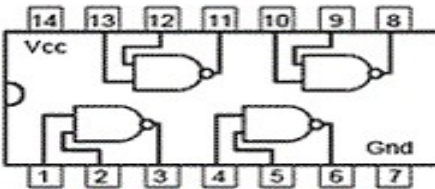
A	B	Output
0	0	1
0	1	0
1	0	0
1	1	1



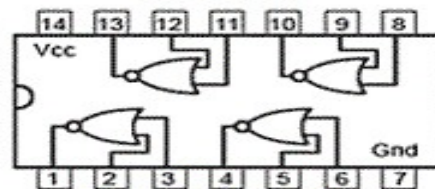
**7408 Quad 2 input AND Gates**



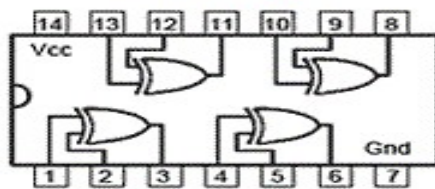
**7432 Quad 2 input OR Gates**



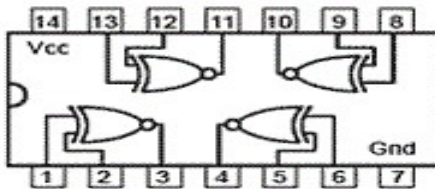
**7400 Quad 2 input NAND Gates**



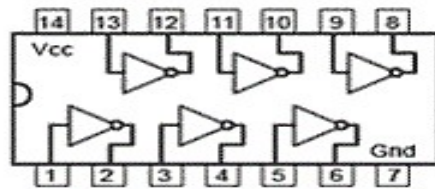
**7402 Quad 2 input NOR Gates**



**7486 Quad 2 input XOR Gates**



**747266 Quad 2 input XNOR Gates**



**7404 Hex NOT Gates (Inverters)**